

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.1030004APPLICATION NO.
To Be AssignedFIRST NAMED INVENTOR
NGUYEN et al.FILING DATE
April 13, 2004ART UNIT
To Be Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
MBS	AA1	3,641,450	02/08/1972	Lunn	—	—	
	AB1	5,625,307	04/29/1997	Scheinerg	—	—	
	AC1	5,668,502	09/16/1997	Rijns	—	—	
	AD1	5,828,265	10/27/1998	Mensink et al.	—	—	
	AE1	4,211,975	07/1980	Kuroda	—	—	
	AF1	5,077,541	12/1991	Gilbert	—	—	
	AG1	5,581,213	12/1996	Linder et al.	—	—	
	AH1	5,715,012	02/1998	Patel et al.	—	—	
	AI1	5,737,035	04/1998	Rotzoll	—	—	
	AJ1	3,564,439	02/16/1971	Rao	—	—	
MBS	AK1	6,509,796	01/21/2003	Nguyen et al.	—	—	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
MBS	AL1	0 587 965 A1	03/23/1994	EP			Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

MBS	AR	1	Furth et al., "Linearised Differential Transducers In Subthreshold CMOS", Electronics Letters, IEEE Stevenage, GB, Vol. 31, No. 7, 30 March 1995, pp. 545-547.
MBS	AS	1	Copy of International Search Report for International Appl. No. PCT/US01/04451, filed February 8, 2001.
MBS	AT	1	Mensink et al., "A CMOS "Soft-Switched" Transconductor and Its Application In Gain Control And Filters", IEEE Journal of Solid-State Circuits, Vol. 32, No. 7, July 1997.

EXAMINER

SHINGLETON

DATE CONSIDERED

7/20/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

MBJ	AR	2	Gilbert, B., "A Low-Noise Wideband Variable-Gain Amplifier Using An Interpolated Ladder Attenuator", IEEE International Solid-State Circuits Conference, U.S. New York, No. 34, February 1991, pp. 280-281 and p. 330.
MBJ	AS	2	Leung L., et al., "10-MHz 60-dB Dynamic-Range 6-dB Variable Gain Amplifier", IEEE, 1997, pp. 173-176.
MBJ	AT	2	Poole, S. et al., "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance", IEEE International Symposium On Circuits And Systems, Finland, Espoo, Vol. 2 of 3, June 7-9, 1988, pp. 1931-1934

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	AA3						
	AB3						
	AC3						
	AD3						
	AE3						
	AF3						
	AG3						
	AH3						
	AI3						
	AJ3						
	AK3						

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

MB)	AR	3	Shin et al., "An Inductorless 900 MHz RF Low-Noise Amplifier In 0.9um CMOS", IEEE 1997 Custom Integrated Circuits Conference, May 5-8 1997, pp. 513-516.
MB)	AS	3	Tham et al., "A Direct-Conversion Transceiver Chip Set For 900MHz (ISM BAND) Spread-Spectrum Digital Cordless Telephone", 1996 IEEE International Symposium On Circuits And Systems, Vol. 4, May 12-15, 1996, pp 85-88.
	AT	3	

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